circuit 42, to output an inner pincushion distortion

correction waveform VAD based on the change in frequency shown

in Fig. 8 (c). The DC correction pulse superimposing circuit

44 superimposes a DC correction pulse on the inner pincushion

5   distortion correction waveform VAD outputted from the

multiplier 43, to output an inner pincushion distortion

correction voltage VA.   In this case, inner pincushion

distortion is corrected by shifting pixels in upper and lower

parts of a vertical line on a screen using the center thereof

10   as a basis.

The horizontal rate correction waveform circuit 41 may

generate the correction waveform in the horizontal scanning

period of time based on the change in frequency shown in Fig.

9 (a), the vertical rate correction waveform circuit 42

15   generates the correction waveform in the vertical scanning

period of time shown in Fig. 9 (b), and the multiplier 43

may generate the inner pincushion distortion correction

waveform based on the change in frequency shown in Fig. 9 (c).

In this case, the inner pincushion distortion is

20   corrected by shifting the pixel at the center of the vertical

line on the screen using the upper and lower ends thereof as

a basis.

In this example, the horizontal rate correction

waveform circuit 41 corresponds to a first correction

25   waveform generation circuit, the vertical rate correction

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